

10/025,843

update

EAST History

5/31/2005

8:53 AM

BRS	4246	(clear or reset 4246) adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	5793	1 or 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	742	3 and cores	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	742	3 and core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	114	5 and bitstream	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	18	6 and FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	114	6 and (Field adj Programmable adj Gate Array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	395	4 and (Field adj Programmable adj Gate Array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
IS&R	73360	((("326") or ("365") or ("716"))).CLAS.	US-PGPUB; USPAT; USOCR
BRS	54	9 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

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EAST History

6/2/2005

2:41 PM

BRS	7054	(clear or reset or erase) adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	740	1 and core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	740	1 and cores	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	11	3 and bitstream	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB